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Sheet	1	of	2

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
H. c.	AA	US- 5,416,437	5/1995	Contreras, et al	
H. c.	AB	US- 5,881,014	03/1999	Ooishi	
H. c.	AC	US- 6,054,887	04/2000	Horie, et al.	
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FOREIGN PATENT DOCUMENTS							
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C m p l t i f K n o w n

Application Number

Filing Date**First Named Inventor**

Srikanth R. Murrar

Art Unit

Examiner Name

Attorney Docket Number

01-B-077C1 (STMI01-00096)

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GREGORIAN, R., et al.; "ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING"; Wiley-Interscience Publication, pages 448-451

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